

#	Name of Organization	Project Title
Category-II		
1	IIT Gandhinagar	Secure and Energy efficient Mixed domain Compute in memory-based AI accelerator Chip for Edge applications
	Sardar Vallabhbhai National Institute of Technology, Surat	
	Dhirubhai Ambani Institute of Information and Communication Technology, Gandhinagar	
	Institute of Technology, Nirma University, Ahmedabad	
2	National Institute of Technology Tiruchirappalli	Ultra Low Power SHAKTI RISC V Based Lightweight Edge AI Processor for IoT enabled Healthcare Applications
	Indian Institute of Information Technology Tiruchirappalli	
	National Institute of Electronics & Information Technology Aurangabad	
Category III		
3	Model Engineering College, Thrikkakara, Kochi	Design and Implementation of IPs for Multiple Sensor Signal Processing and RISC-V based Hardware Accelerator for Object Detection and Tracking in ADAS Applications
4	Anna University Regional Campus Coimbatore	ASIC Realization of Light Weight Hardware Security Protocol architecture for RFID Enabled Applications
5	Shri Vishnu Engineering College for Women, Bhimavaram, West Godavari District	Memory-Optimized Co-Processing Unit for Enhanced Edge AI